Application No.: 10/774,517 Docket No.: 8733.345.10

Application No.: 10/774,517

Amendment dated January 9, 2008

Response to Office Action dated October 9, 2007

REMARKS

At the outset, the Examiner is thanked for the thorough review and consideration of the pending application. The Office Action dated October 9, 2007 has been received and its contents carefully reviewed.

By this Amendment, Applicant has amended claims 11, 16 and 19, and no new matter has been added. Accordingly, claims 11-14 and 16-21 are currently pending. Reexamination and reconsideration of the pending claims is respectfully requested.

In the Office Action, the Examiner rejected claims 11-14 and 16-21 under 35 U.S.C. §103(a) as being unpatentable over <u>Wakai et al.</u> (U.S. Patent No. 5,166,085) in view of <u>Kitazawa</u> et al. (U.S. Patent No. 5,920,082) and <u>ARA</u> (Applicant's Related Art (Figures 2 and 4)).

The rejection of 11-14 and 16-21 under 35 U.S.C. §103(a) as being unpatentable over Wakai et al. in view of Kitazawa et al. and ARA is respectfully traversed and reconsideration is requested. Claims 11 is allowable over the cited references in that claim 11 recites, for example, a combination of elements, "...forming a gate pad and a data pad, at least one of the gate pad and the data pad having a pad hole therein;...forming a protection layer on the thin film transistor and on the gate insulation layer, wherein forming the protection layer includes etching the protection layer to cover the first side but not the second side of the drain electrode, and wherein the protection layer includes a contact hole exposing the pad hole..." None of the cited references teach or suggest at least this feature of the claimed invention. Accordingly, Applicant respectfully submits that claim 11 and claims 12-14 and 16-21, which depend therefrom, are allowable over the cited references.

The Examiner states at page 3 of the Office Action that "APA's figures 2 and 4 do disclose that a gate/data line (21/24) having a gate/data pad (60/62) electrically contacting a gate/data pad electrode (62/60) through a gate/data pad hole (21/23) as claimed." It appears that the Examiner recites the reference numbers 21 and 23 as the gate pad hole and the data pad hole, respectively. However, the reference numbers 21 and 23 indicate the gate pad and the data pad, respectively. See, for example, paragraph [0027] and figure 4. Further, the gate pad 21 and the data pad 23 do not have any hole therein.

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Applicant believes the application is in condition for allowance and early, favorable action is respectfully solicited. If for any reason the Examiner finds the application other than in condition for allowance, the Examiner is requested to call the undersigned attorney at (202) 496-7500 to discuss the steps necessary for placing the application in condition for allowance. All correspondence should continue to be sent to the below-listed address.

If these papers are not considered timely filed by the Patent and Trademark Office, then a petition is hereby made under 37 C.F.R. §1.136, and any additional fees required under 37 C.F.R. §1.136 for any necessary extension of time, or any other fees required to complete the filing of this response, may be charged to Deposit Account No. 50-0911. Please credit any overpayment to deposit Account No. 50-0911.

Dated: January 9, 2008

Respectfully submitted,

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